Seat No.:	Enrolment No.

GUJARAT TECHNOLOGICAL UNIVERSITY

	•	BE - SEMESTER-III (OLD) - EXAMINATION – SUMMER 2017 t Code: 130902 t Name: Analog and Digital Electronics	17
Ti	me: i	10:30 AM to 01:00 PM ons: Attempt all questions. Make suitable assumptions wherever necessary.	70
Q.1	(a) (b)	Explain the block diagram of op-amp with function of each block. What are ideal characteristics of operational amplifier?	07 07
Q.2 ((a)	Describe inverting and noninverting amplifier using op-amp with relevant circuit diagrams, waveforms and equations.	
	(b)	Explain the working of op-amp as integrator and differentiator. OR	07
	(b)	Describe working of a differential amplifier using three op-amps.	07
Q.3	(a)	Explain the basic principle of comparator. Describe the working of Schmitt trigger circuit.	07
	(b)	Discuss summing, scaling and averaging amplifier. OR	07
Q.3	(a) (b)	Discuss IC555 with function of each pin. Write a brief note on instrumentation amplifier using op-amp.	
Q.4	(a) (b)	Describe the working of a 3 to 8 line decoder. Explain a 4 input multiplexer.	
Q.4	(a) (b)	OR Explain JK master slave flipflop. Define the following terms w.r.t. flip flops: set up time, propagation delay time, hold time, preset, clear, race around, truth table.	
Q.5	(a) (b)	Discuss 78XX and 79XX three terminal voltage regulators. State and explain De Morgan's theorems. OR	
Q.5	(a) (b)	Reduce using K-map (i) Σ m(5,6,7,9,10,11,13,14,15) (ii) Π M(1,5,6,7,11,12,13,15). Discuss working of IC555 with function of each pin.	07 07
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